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LIST OF PRIOR ART CITED BY APPLICANT
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Applicant
Satoru Kawakami

Filing Date
11/07/01

Group

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
PMW	AA	5 4 8 3 4 1 3	01/09/96	Babb	361	220	10/24/94
	AB						
	AC						
	AD						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
PMW	AE	1 9 6 4 9 4 33	11/28/96	German Patent	H05K	9/00		X
PMW	AF	1 9 6 3 0 7 20	7/30/96	German Patent	H05K	1/16		X
PMW	AG	0 9 3 2 2 5 2	01/21/99	European Patent	H03D	7/16	X	
	AH							
	AI							

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

PMW	AL	Armstrong, M.K, PCB design techniques for lowest-cost EMC compliance: Part 1, <i>Electronics & Communication Engineering Journal</i> , August 1999, pp.185-194					
	AM						
	AN						
	AO						
	AP						
	AQ						

EXAMINER

DATE CONSIDERED

9/10/04

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